

LIST OF PATENTS  
AND PUBLICATIONS

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APPLICANT

Luciano LENZINI et al

FILING DATE

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GROUP

2616

## U.S. PATENT DOCUMENTS

EX. INIT		DOCUMENT NO. Cntry code - No.	DATE MM-YYYY	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
/KY/	AA	US-6 147 970	11/14/00	TROXEL			
/KY/	BB	US-6 091 709	07/18/00	HARRISON			
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## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO. Cntry Code - No.	DATE MM-YYYY	COUNTRY	NAME	CLASS	TRANSL.	
							YES	NO
/KY/	AI	1 035 751	09/13/00	EUROPE	AUKIA			
/KY/	AJ	02/ 35 777	05/02/02	WO	LENZINI			
	AK							
	AL							
	AM							
	AN							
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## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AP	"CARRY-OVER ROUND ROBIN: A SIMPLE CELL SCHEDULING MECHANISM FOR ATM NETWORKS" D. Saha et al, 8 pages
AS	"A POLYNOMIAL-TIME OPTIMAL SYNCHRONOUS BANDWIDTH...." Ching-Chih HAN et al; Real-Time Computing La.; XP000580660; 8 pages
AT	"DELAY ANALYSIS OF THE FDDI SYNCHRONOUS DATA CLASS"; Wayne L.Genter et al; CH2826-5/90/0000/0766; 8 pages
AU	"ON THE EFFECTIVENESS OF THE FDDI-M MEDIUM ACCESS PROTOCOL...."; Journal of Supercomputing, 11:371-389 (1997), Edward CHAN et al; pages 371-389

EXAMINE

/Kan Yuen/

DATE CONSIDERED

05/03/2007

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12 October 2004

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